

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FIRST NAMED INVENTOR: Arkadiy PEKER

SERIAL NUMBER: 11/091,675 Patent Number: 7,466,819

FILING DATE: Jan 22, 2004 EXAMINER: Parries, Dru M

TITLE: High Power Architecture for Power over Ethernet
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SHOWING UNDER 35 U.S.C. 255 IN SUPPORT OF REQUEST FOR CERTIFICATE OF
CORRECTION

Sir:

The attached request for certificate of correction, and fee due under 37 CFR 1.20(a) is addressed to claims 1, 23, 33 and 41 of the above subject patent.

In claims 1 and 41 the word "said" has been inadvertently added before the phrase "at least a portion". The term at least a portion first appears in this clause, and thus the term "said" is inappropriate, and the clear meaning is without the term "said". Such an error is of minor character, does not constitute new matter and does not require reexamination, thus meeting the requirements under 35 U.S.C. 255.

In claim 23, the claim was inadvertently set dependent on claim 1, however claim 1 does not recite the antecedent current share circuit, which only appears in the preceding claim 22. Such an error is of minor character, does not constitute new matter and does not require reexamination, thus meeting the requirements under 35 U.S.C. 255.

The correction to claim 33 is a clerical error on the part of the USPTO, where the term PWM/resonance has been transcribed to PWJVI/resonance

Respectfully submitted,



Simon Kahn
Registration No. 48,249

March 19, 2009

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CERTIFICATE OF CORRECTIONPage 1 of 1

PATENT NO. : 7,492,059

APPLICATION NO.: 10/761,327

ISSUE DATE : Feb 17, 2009

INVENTOR(S) : Arkadiy Peker; Dror Korcharz; Alon Ferentz; Yair Darshan

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 22, (Claim 1), line 2 to read - receive said first power "over at least" a portion of said

Column 22, (Claim 1), line 4 to read - receive said second power "over at least" a portion of

Column 23, (Claim 23), line 27 to read - A local area network according to "claim 22"

Column 24, (Claim 33), line 15 - 16 to read - further comprises a first "PWM/resonance controller"

Column 25, (Claim 41), line 35 to read - receive said first power "over at least" a portion of said

Column 25, (Claim 41), line 37 to read - receive said second power "over at least" a portion of

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Microsemi Corp. - c/o Landon IP, Inc.
1700 Diagonal Road - Suite 450
Alexandria, VA 22314-2866

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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